

# **IES / GATE**

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**Electronics &  
Telecommunication  
Engineering**

**VOLUME-III**

**Network Theory**



# Contents

**Network Theory**

**1-283**



## NETWORK THEORY

1. Basics
2. Steady state A.C. Circuits [Resonance]
3. Theorems
4. Transients
5. Two-port
6. Graph Theory
7. Magnetic Coupled Circuits [\*Not in Syll\*]
8. Filters [\*Not in Syll\*]

} frequently asked  
 } -

• Fundamentals of Electric Circuits :-

by Alexander & Sadiku [\*only exercises]

- Engg Circuit Analysis :- Hayt & Kemmerly
- Network Analysis [Transients and two port  $\Rightarrow$  conventional] :- Van Valkenberg

1] Gate - ECL

Problems

3] Workbook

2] IAS

2] IES  $\left\{ \begin{array}{l} \text{Memory (40\% repeated)} \\ \text{Problems} \end{array} \right.$

4] Home work

6] Text - series  $\left\{ \begin{array}{l} \rightarrow \text{old} \\ \rightarrow \text{new} \end{array} \right.$

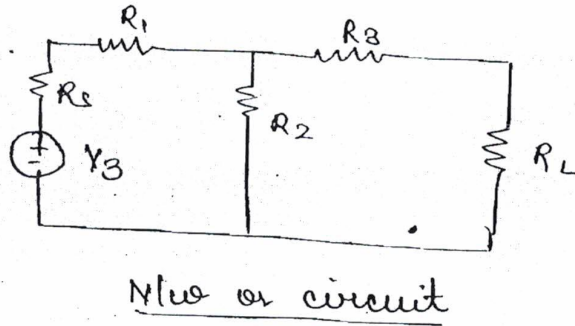
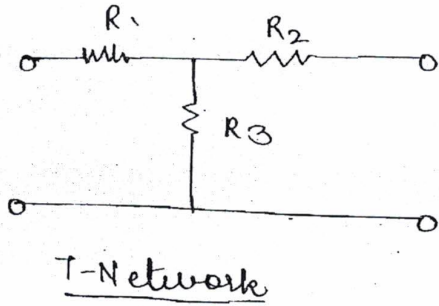
\* [goo.gl/2XhUx3](http://goo.gl/2XhUx3)  $\rightarrow$  sadiku  $\left\{ \begin{array}{l} \rightarrow \text{Textbook} \\ \rightarrow \text{soln} \end{array} \right.$

\* [goo.gl/HWKRZj](http://goo.gl/HWKRZj)  $\rightarrow$  Home work (Nodal + Mesh analysis)

\* [goo.gl/6Fa34E](http://goo.gl/6Fa34E)  $\rightarrow$  steady state + Theorems

\* [goo.gl/dTnhu8](http://goo.gl/dTnhu8)  $\rightarrow$  RL, RC, RLC  $\rightarrow$  conv.

• Network vs Circuit :->

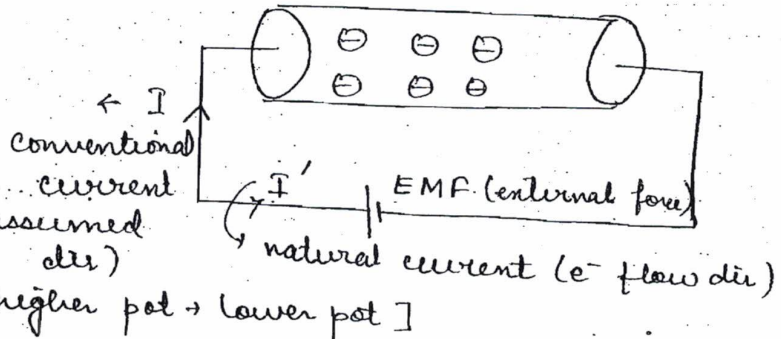


- Network is a combination of elements, it may or may not consist of closed path
- Circuit is a combination of elements and it should consist of closed path.
- All circuits are Network but all Networks are not circuits.

•  $q = -1.602 \times 10^{-19} \text{ C}$

$I = \frac{dQ}{dt} \frac{\text{C}}{\text{s}} \text{ or A}$

rate of flow of charge with time



$Q = \int I \cdot dt$

[higher pot -> lower pot]

In KCL, KVL we always use conventional direction

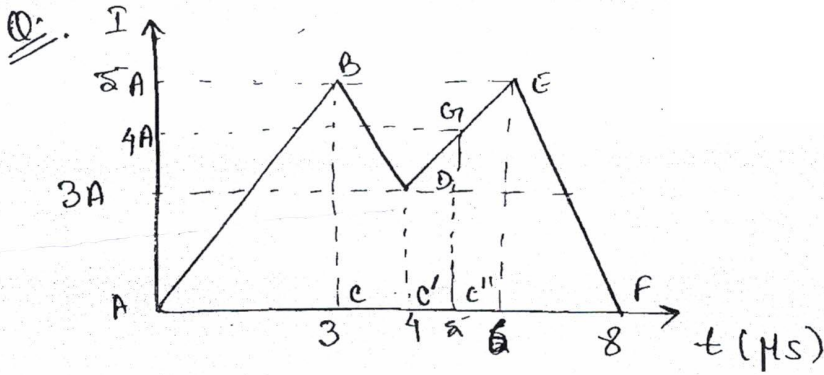
$|I| = I'$  but opposite in direction.

$Q = \int_{-\infty}^t I \cdot dt = \int_{-\infty}^0 I \cdot dt + \int_0^t I \cdot dt \text{ or } Q = Q_0 + \int I \cdot dt$

$V = \frac{dW}{dQ} = \frac{\text{J}}{\text{C}} \text{ or Volt}$

$P = \frac{dW}{dt} \frac{\text{J}}{\text{s}} \text{ or Watt}$  → time rate of energy.

$= \frac{dW}{dQ} \times \frac{dQ}{dt} \therefore P = VI = I^2R = \frac{V^2}{R} = \frac{67V^2}{R} = I^2/67$



Find charge acquired by cap in  $-2 \mu\text{sec}$

$\Rightarrow Q = \int I dt$

2 methods: ① Developing mathematical eq<sup>n</sup>  
 ② Finding area under curve.

2.  $Q = \int I dt$

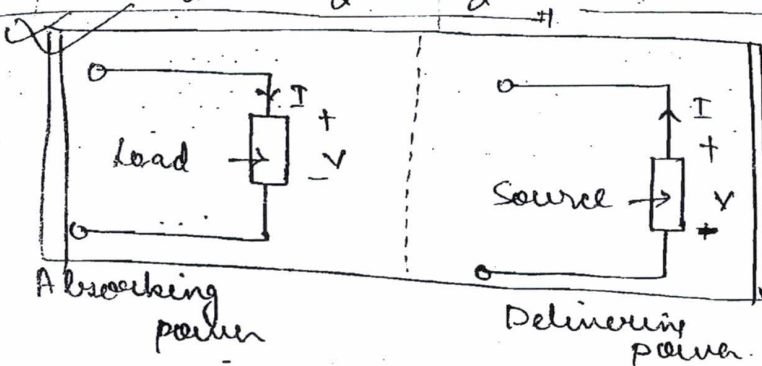
$$= \Delta ABC + \text{trapezoid } BDC'C + \text{trapezoid } c'Dc''c$$

$$= \frac{1}{2} (3 \times 5) + \frac{1}{2} \times (3+5) \times 1 + \frac{1}{2} \times (3+4) \times 1$$

\* Area of Trapezoid =  $\frac{1}{2} \times (\text{sum of ht of 2 sides}) \times \text{dist b/w sides}$

\* DE = linear, slope constant  $m=1 \therefore$  at  $5 \mu\text{s}$ ;  $I = 4A$

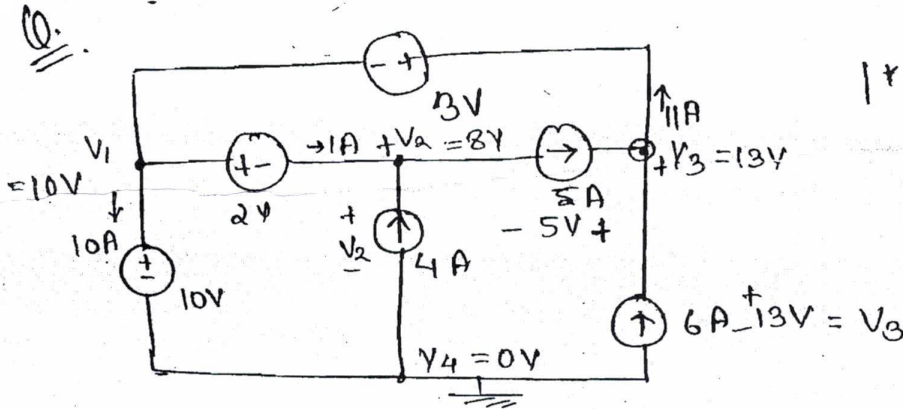
$$= \frac{1}{2} \times 15 + \frac{1}{2} \times 8 + \frac{1}{2} \times 7 = 7.5 + 4 + 3.5 = 15 \mu\text{C}$$



$\Rightarrow$  Absorbing + Delivering  
Source / Load (element)

\* When the current is entering at the terminal element is absorbing power.

\* When the current is leaving from the terminal element is delivering power



\* To find power, find voltage & current of each element \*

Find power of each element of the N/w shown.

$\Rightarrow$  potential b/w  $V_1$  &  $gnd = 10V \therefore V_1 = 10V$

at node  $V_1$  &  $V_2$  :-  $V_1 - V_2 = 2V \therefore V_2 = 8V$

at node  $V_1$  &  $V_3$  :-  $V_3 - V_1 = 3V \therefore V_3 = 13V$

In 4A :-  $P = 4 \times 8 = 32W$  (Delivering)

In 6A :-  $P = 6 \times 13 = 78W$  (Delivering)

In 5A :-  $P = 5 \times 5 = 25W$  (Delivering)

} current leaving  
 +ve terminal

In 3V :-  $P = 11 \times 3 = 33W$  (Absorbing)

In 2V :-  $P = 1 \times 2 = 2W$  (Absorbing)

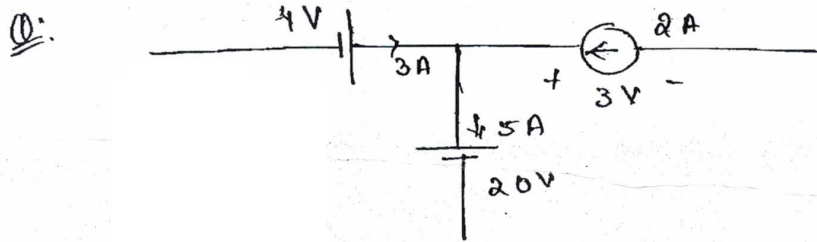
In 10V :-  $P = 10 \times 10 = 100W$  (Absorbing)

}  $\pm$  entering +ve  
 terminal or going  
 from lower to higher  
 pot.

$(P_+)_\text{Absorbing} = (P_+)_\text{Delivering}$

$P_{T \text{ del}} = 32 + 78 + 25 = 135W$   
 $P_{T \text{ abs}} = 33 + 2 + 100 = 135W$





Find total power absorbed in the figure shown.

In 4V:  $P = 4 \times 3 = 12W$  (Del) =  $-12W$  (Abs)

In 2A:  $P = 2 \times 3 = 6W$  (Del) =  $-6W$  (Abs)

In 20V:  $P = 20 \times 5 = 100W$  (Absorbing)

$P_T = 82W$  (Absorbing)

The capacity to do work is called energy.

$$W = \int_0^t P dt$$
 watt-sec  
 or Joule

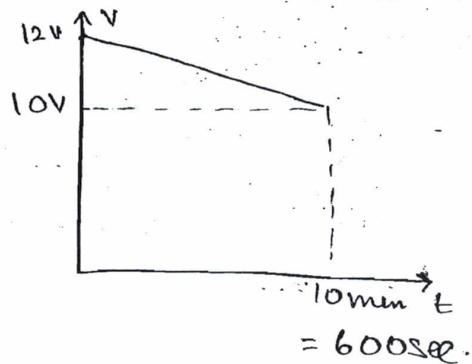
Q. A fully charged mb. phone is good for 10 min talktime. During talktime battery delivers a constant current of 2A. Find energy on the battery during talktime.

$$\begin{aligned} \Rightarrow W &= \int_0^t P dt = \int_0^t VI dt \\ &= I \int_0^t V dt \quad I = 2A \text{ (constant)} \end{aligned}$$

$$= 2 \times \frac{1}{2} \times (12 + 10) \times 600$$

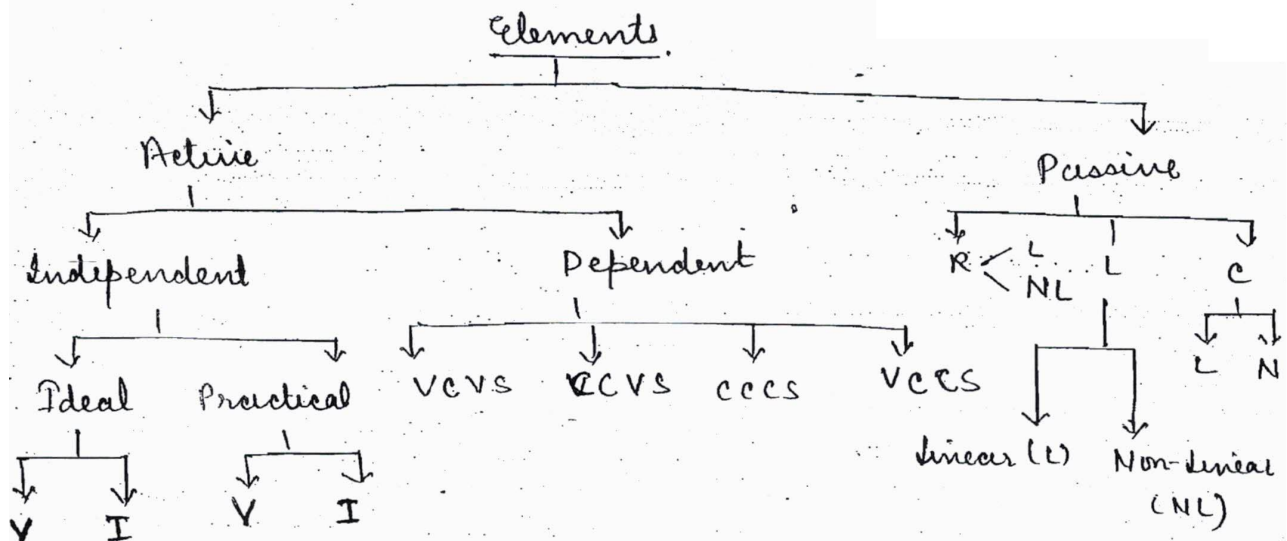
$$= 22 \times 600$$

$$= 13200 = 13.2 \text{ kJ}$$



## # Classification of Elements:-

1. Active + Passive elements:-
2. Linear and Non linear Elements
3. Unidirectional and bi-directional elements
4. Time Variant and Invariant
5. Lumped + Distributed



• **ACTIVE ELEMENT:** When the element is capable of delivering energy independently for long time (approximately infinite time). or

• When the element is having property of internal amplification.

eg:- Voltage source (V), Current source (I), } Independent.  
 Transistor + op-amp. } Dependent

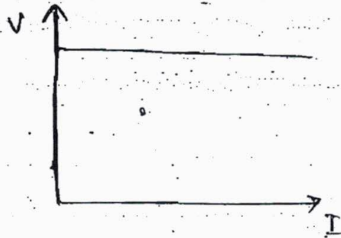
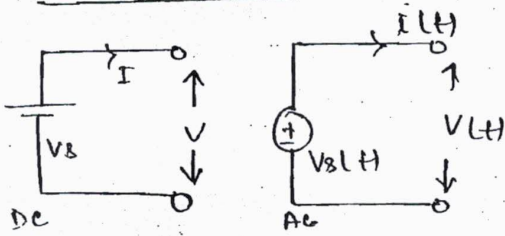
\* Cap vs independent sources:

• During discharging capacitor can deliver the energy independently for short time<sup>①</sup> and capacitor is not having internal amplification<sup>②</sup> property

• **PASSIVE ELEMENT**:- when the element is not capable of delivering energy independently.

eg:- Resistor, bulb, transformer  $(V_1 I_1 = V_2 I_2) \Rightarrow$  no internal amplification  
 $P_{in} = P_{out}$

• Ideal V source:



DC  $\rightarrow$  no time mentioned  
 AC  $\rightarrow$  time mentioned

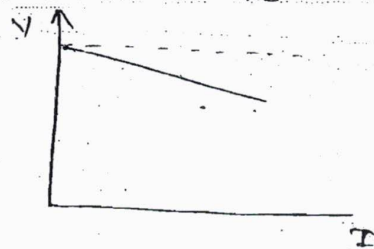
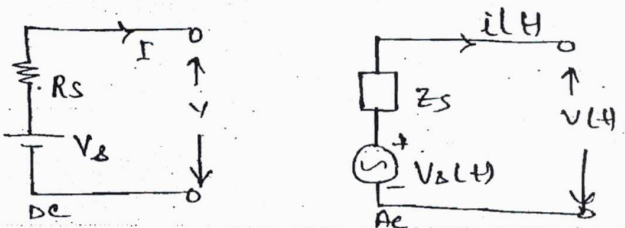
$R_s = 0$      $V = V_s$      $V \neq f(I)$

$\rightarrow$  Ideal voltage source delivers energy at specified voltage (V) which is independent on current delivered by source.

$\rightarrow$  Internal Resistance of ideal voltage source = 0.

Independent voltage source does not satisfy ohm's law since VI characteristics is not linear. (does not cross origin or not linearly?)

• Practical V source:



For DC:  $V_s = V + I R_s$

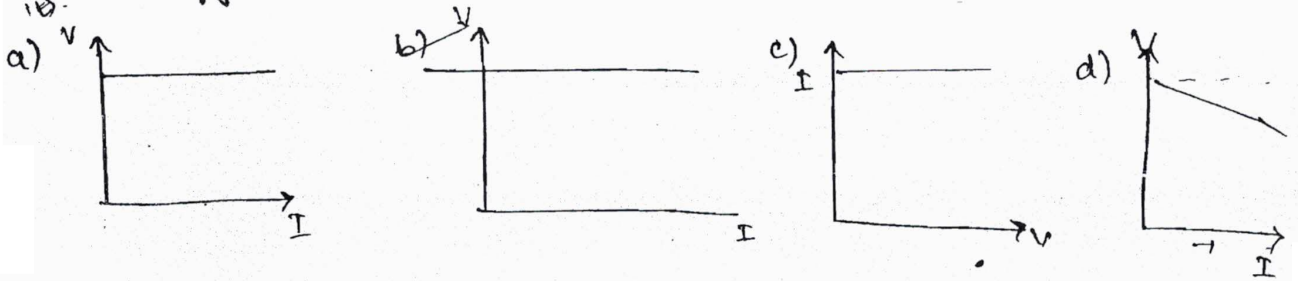
$V = f(I)$  or  $V = V_s - I R_s$      $V \downarrow$  as  $I \uparrow$

$\rightarrow$  Practical voltage source delivers energy at specified voltage (V) which depends on current delivered by the source.

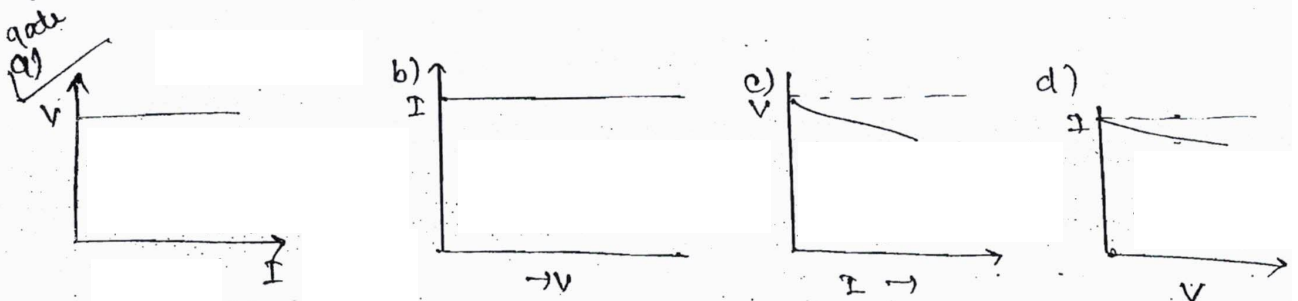
$\rightarrow$  Internal Resistance of practical voltage source =  $R_s$

$R_s \neq 0$

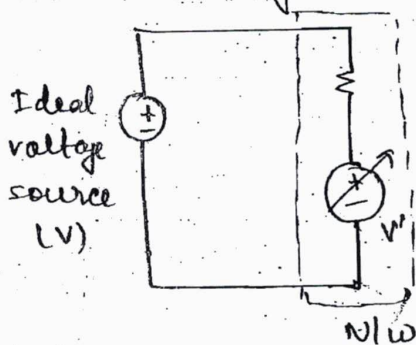
Q. Identify  $V-I$  characteristics of ideal voltage source



Q. Same - Identify  $V-I$  charact of ideal volt source.



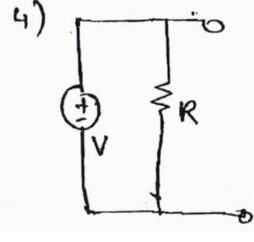
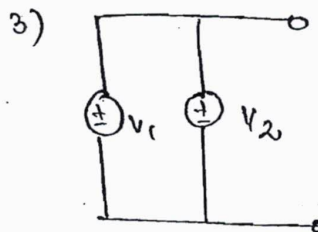
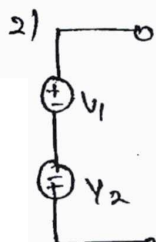
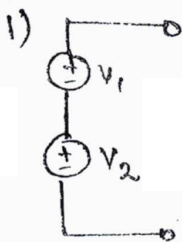
∴ Considering a simple example-

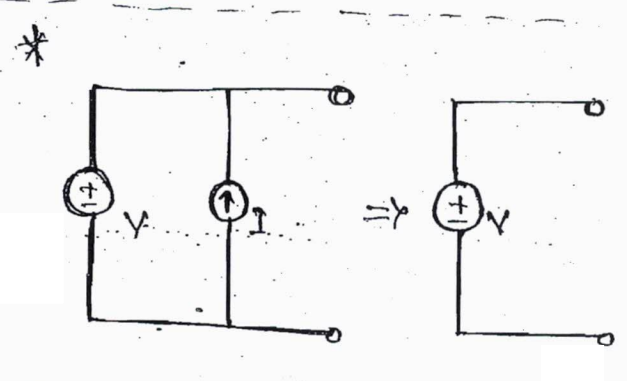
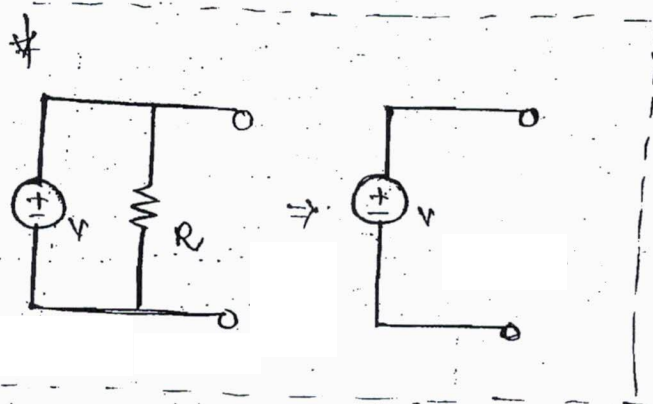
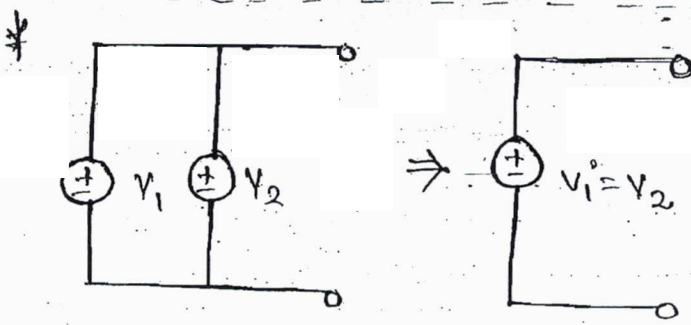
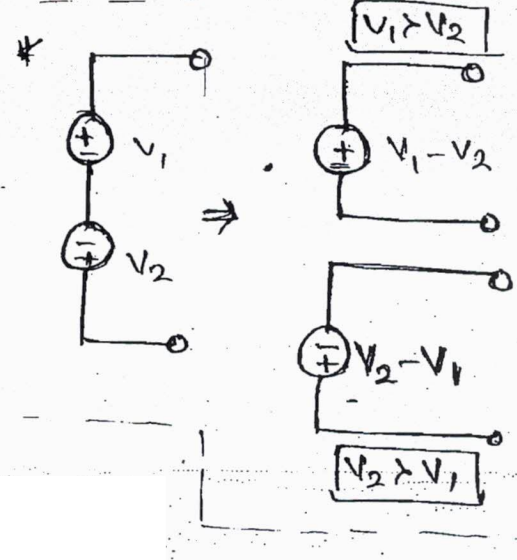
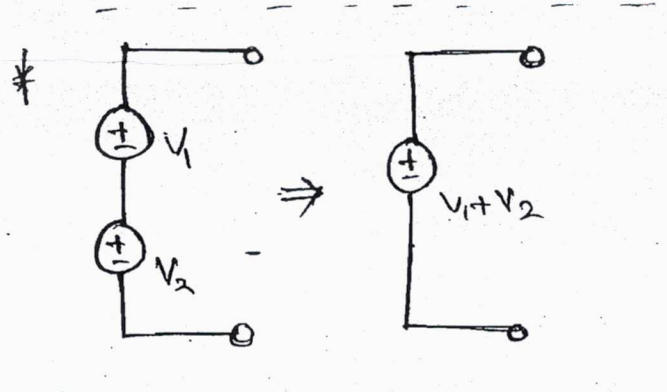
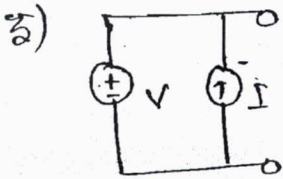


if  $V' > V \Rightarrow I = (\text{clockwise})$   
 $V' < V \Rightarrow I = (\text{anticlockwise})$   
 $V' = V \Rightarrow I = 0$   
 ∴ for both directions of  $I$ ,  $V$  is constant, hence  $I$  considers in both +ve & -ve direction.

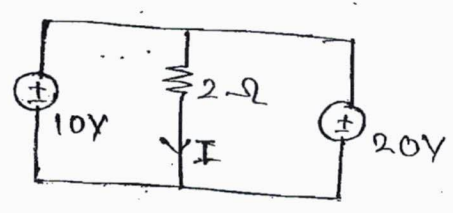
If both options  $Q_1(a, b) \rightarrow$  select b  $\rightarrow$  most appropriate  
 one option  $Q_2(a) \rightarrow$  select a.

Q. Find equivalent ct corresponding to them:-



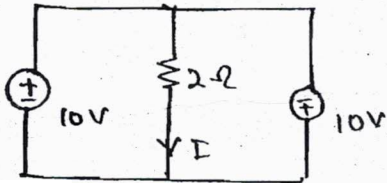


Q. Find current in the  $2\Omega$  resistance



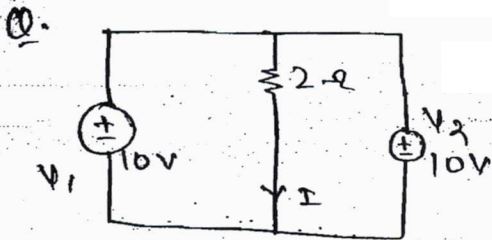
- a) 5A      b) 10A
- c) 15A     d) None or
- (NOT satisfying KVL) or
- (0)

**NOTE:-** With respect to KVL, voltage across all the parallel branches should be equal.

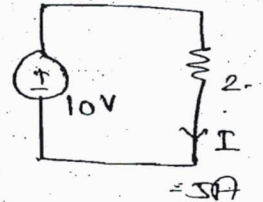


⇒ Polarity as well as magnitude of voltages in parallel branches should be equal. Hence,

NOT satisfying KVL  $\therefore I = 0$   
 $10V \neq -10V$

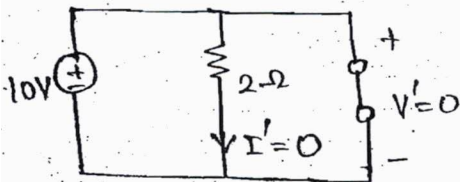


$10V = IR$   
 $\therefore I = \frac{10V}{2} = 5A \Rightarrow$



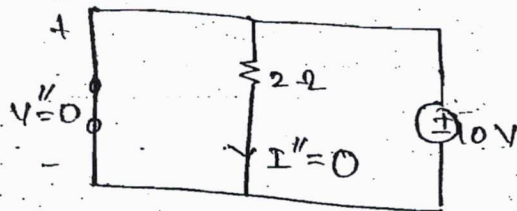
• Verifying by using superposition theorem:-

Case 1 due to  $V_1$ ;



$I = I' + I'' = 0$

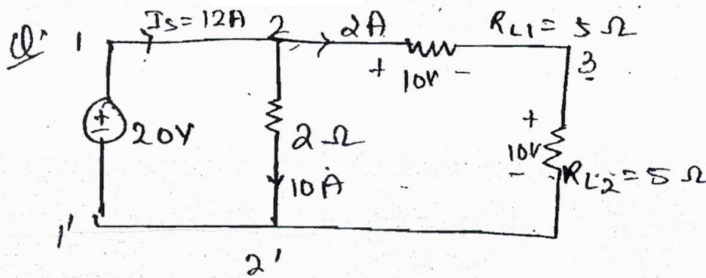
Case 2 due to  $V_2$ .



$10V \neq -10V \rightarrow$  not satisfying KVL

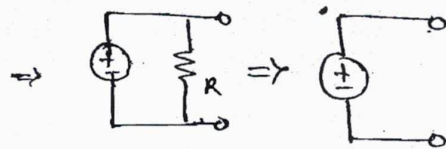
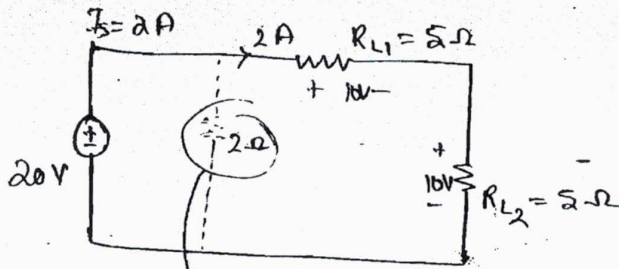
$10V + V' \neq \therefore$  KVL not satisfied  $\leftarrow \therefore$  The superposition does not apply.

**NOTE:-** For the above Nilu superposition theorem cannot be applied since Case 1 + Case 2 circuits are not satisfying KVL.



$$V_{11'} = V_{22'} = V_{232'}$$

↓  
parallel branches



$R$  not important/justifying ckt (4) from previous.

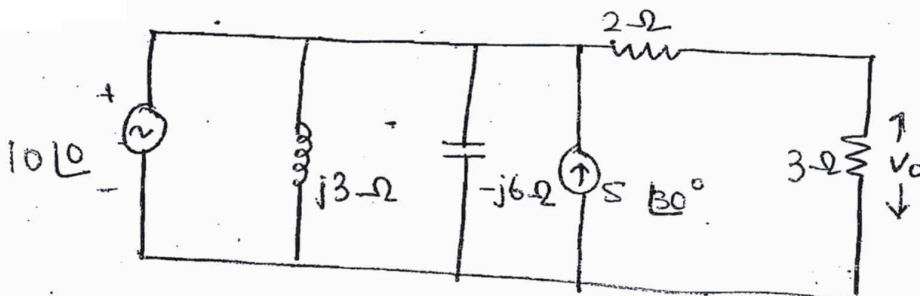
In 1<sup>st</sup> ckt;  $P_s = 20 \times 12 = 240W$   
 In 2<sup>nd</sup> ckt;  $P_s = 20 \times 2 = 40W$  } → not justifying, ckt (4)

∴ Conclusion: →  $R$  can be neglected w.r.t. load calculations ( $V_L, I_L$ )  
 $R$  cannot be neglected w.r.t. source current & source power

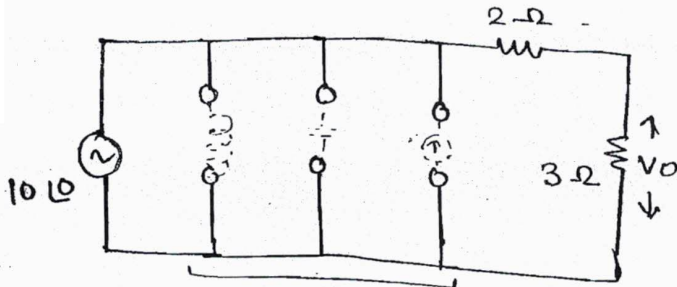
**NOTE:-** In above N/w,  $2\Omega$  resistance ( $10A$  current) can be neglected to calculate as a load current or load voltage.

However, in above N/w,  $2\Omega$  resistance ( $10A$  current) cannot be neglected to calculate either source current or source power.

Q. Find  $V_o$  on the n/w shown.



⇒ Equivalent ckt for solving can be reduced to



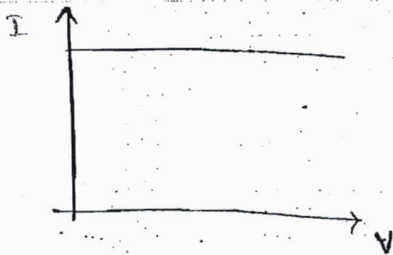
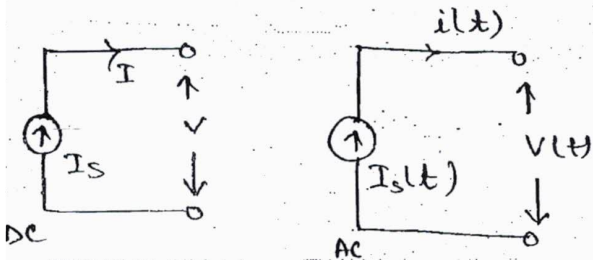
$$I = \frac{10}{5} = 2 \text{ A}$$

$$V_0 = I \times 3 \Omega$$

$$V_0 = 6 \text{ V} \text{ (ans)}$$

neglected as in parallel.

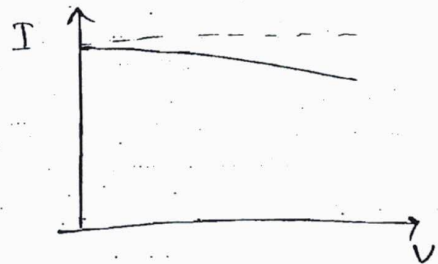
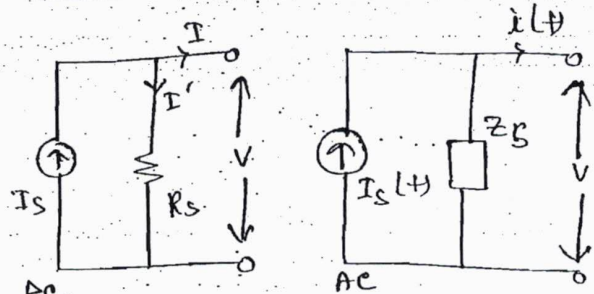
### • Ideal Current Source -



$$R_s = \infty$$

- Ideal current source delivers energy at specified current (I) which is independent of voltage across the source.
- Internal resistance of ideal current source =  $\infty$

### • Practical Current Source -



$$I_s = I' + I$$

$$\Rightarrow I = I_s - I' \text{ or } I = I_s - \frac{V}{R_s}$$

$$I \propto V$$

$$I = I_s - \frac{V}{R_s}$$

- Practical current source delivers energy at specified current (I) which depends on voltage across the source.

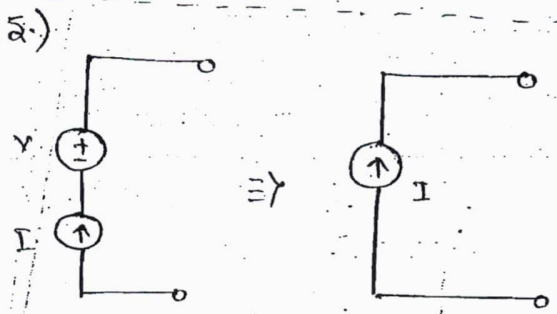
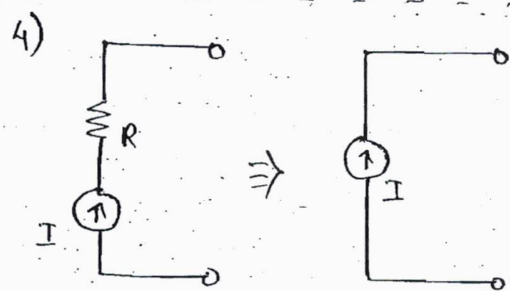
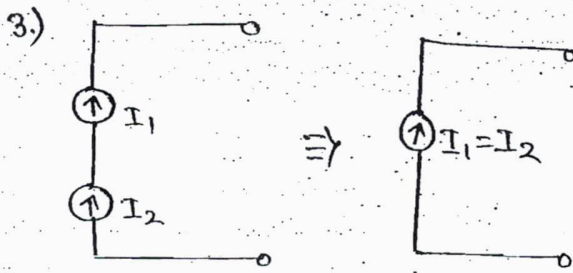
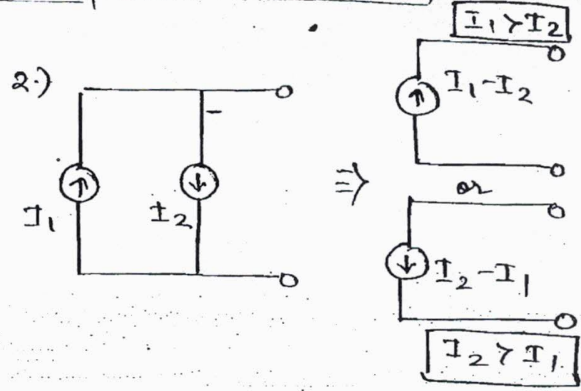
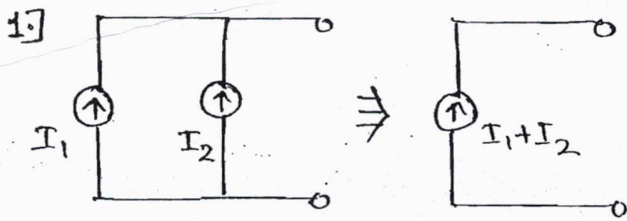
Conclusion:- Independent current source, does not obey Ohm's law, since VI characteristics is non-linear.



- ✓ Internal resistance of ideal voltmeter =  $\infty$
- ✓ " " " ideal ammeter =  $0$   $\rightarrow R_s$  (ideal)

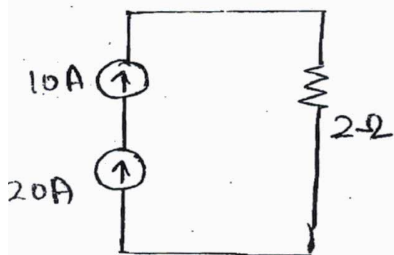
✓ In the practical system independent current source does not exist but dependent current source exists.

eg:- Transistor  $\rightarrow$  theoretical concept for source transformation.



← Equivalent topologies for various NWS with ideal current source

Q. Find equivalent current in  $2\Omega$  resistor.



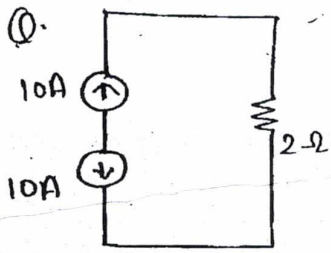
a) 10A

b) 20A

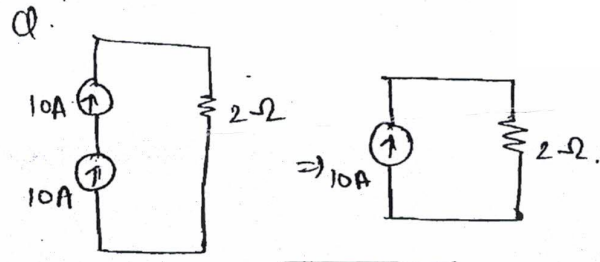
c) 30A

✓ None or Not satisfying KCL

**NOTE:-** With respect to KCL, current flowing through all the series elements should be equal.

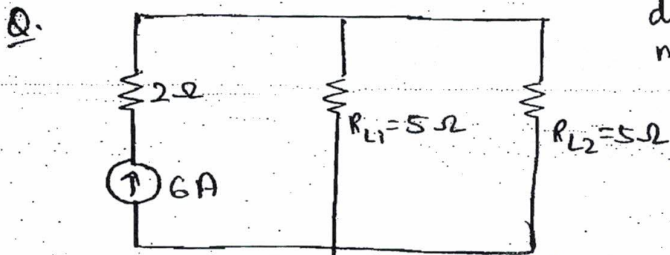


not satisfying KCL at both direction + magnitude should be same.

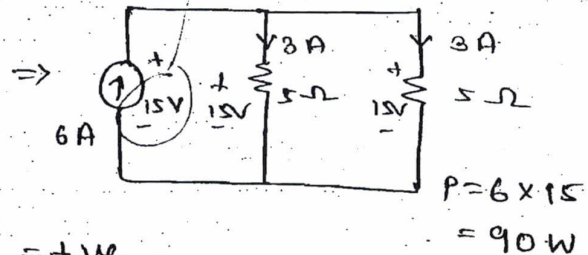
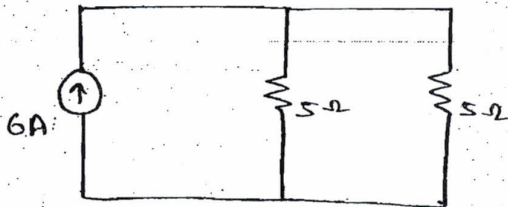
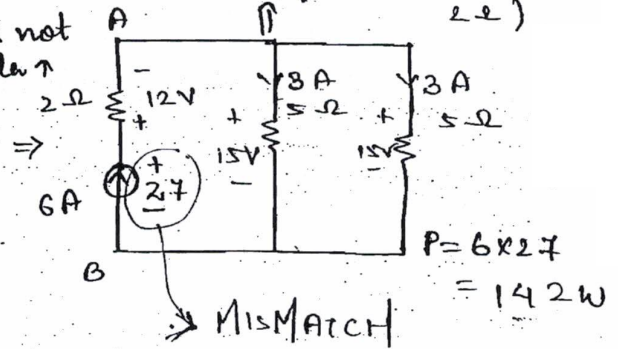


$I = 10A$

Property (4) for load is verified (with without  $2\Omega$ )



does not matter  $\uparrow$



Notations:- when current entering = +ve corresponding to arrow  $\uparrow \oplus$

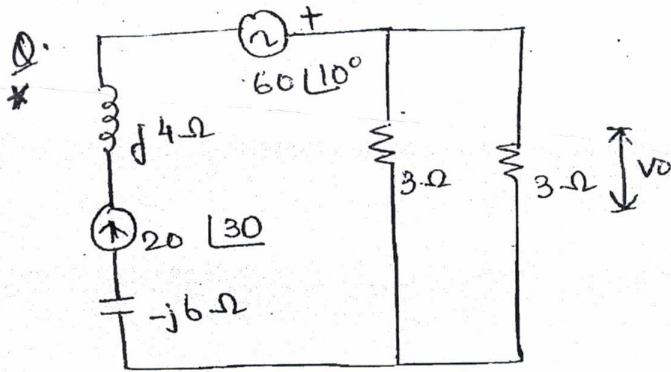
$V_{AB} = V_S - 12 \Rightarrow 15 = V_S - 12 \Rightarrow V_S = 27$  ( $V_S > 12$ )  $\rightarrow$  considered as source volt is greater.

**NOTE:-** In above N/w:-

$2\Omega$  resistance (12V voltage) Can be neglected wrt load calculation  $\parallel (V_L + I_L)$

but it cannot be neglected wrt source calculations [current, <sup>source</sup> voltage or power of current source]

$\parallel (I_{IS}, P_{IS})$



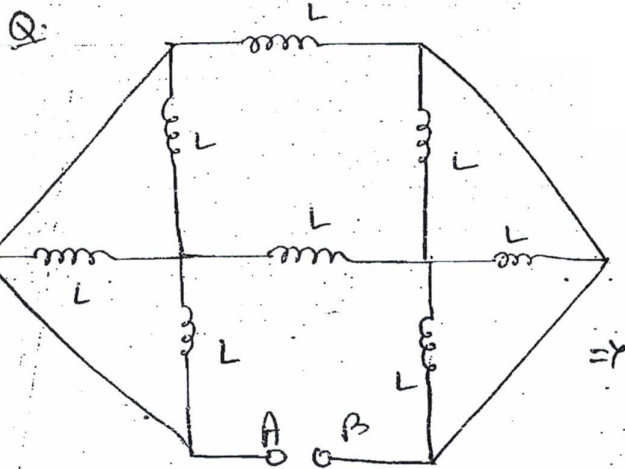
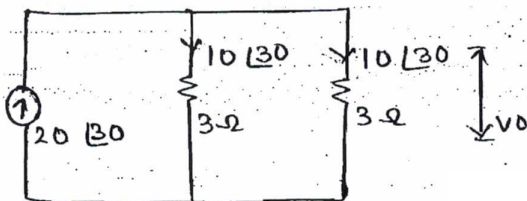
Find  $V_0$ .

\* All elements in series to current source are neglected \*

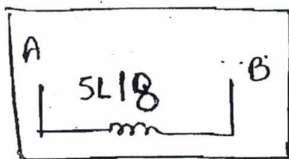
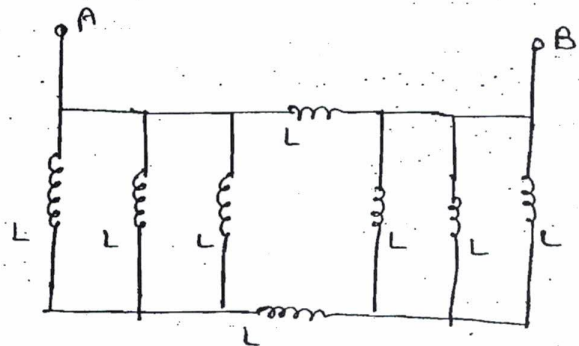
Equivalent ckt  $\rightarrow$

$$\therefore V_0 = 3 \times 10 \angle 30^\circ$$

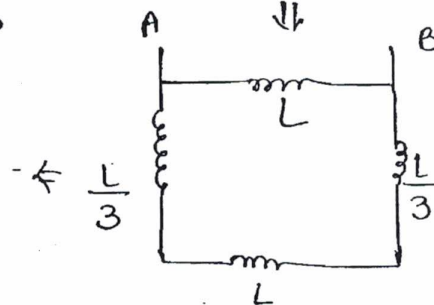
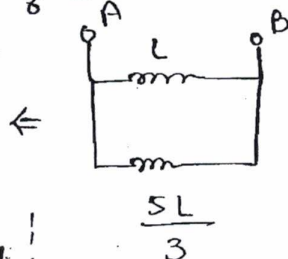
$V_0 = 30 \angle 30^\circ \text{ V}$



1) Find equivalent inductance w.r.t A+B.



$$L_{eq} = \frac{5L}{3}$$



$$L \parallel \frac{5L}{3} \Rightarrow \frac{L \times \frac{5L}{3}}{L + \frac{5L}{3}} = \frac{L^2 \frac{5}{3}}{4L} = \frac{5L}{8} \left( \frac{2}{3} + L + \frac{L}{3} \right)$$